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DOCUMENT-IDENTIFIER:

US 5774321 A

TITLE:

Power supply circuit with load

protection function

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Detailed Description Text - DETX (22):

If a short-circuit occurs in the load and the current flowing through the

load exceeds a predetermined level which is determined by the value of

resistors R1 through R4, the voltage drop .DELTA.V across current sense $\,$

resistor Rs causes the voltage $\mbox{\sc Va}$ to become greater then the voltage $\mbox{\sc Vb.}$ This

in turn, causes the output of comparator U3 to switch from a high logic level

to a low-logic level, thereby causing latch 130 to generate a latch signal

which has a low-logic level. The AND gate U10 then output a pulse signal as

shown in waveform (b) of FIG. 4, and the AND gate U9 outputs a low level signal $\,$

which turns off transistor TR1, thereby turning the switching device M1 off.

As a result, the power supply from the power source 100 to the load 200 is interrupted.